

JVC

SCHEMATIC DIAGRAMS

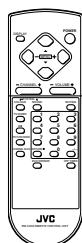
COLOUR TELEVISION

BASIC CHASSIS

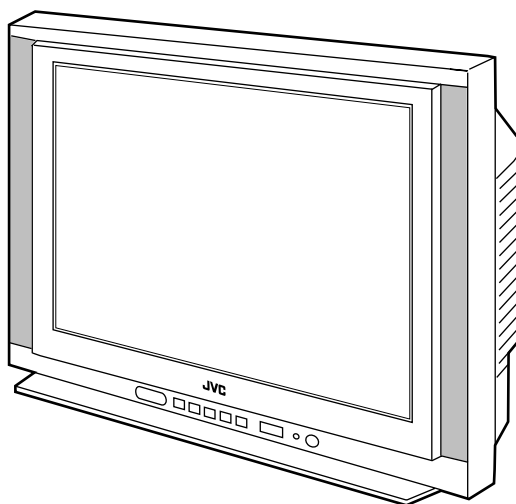
CH

AV-21L81 (-BK)

CD-ROM No. SML200110



RM-C359-1C



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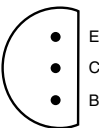

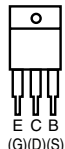
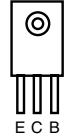

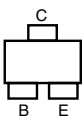
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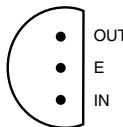
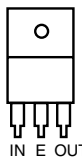
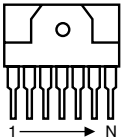
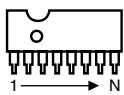
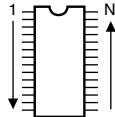
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SEMICONDUCTOR SHAPES

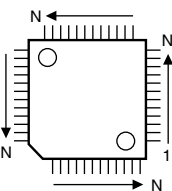
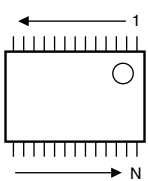
TRANSISTOR

BOTTOM VIEW	FRONT VIEW				TOP VIEW
					CHIP TR 

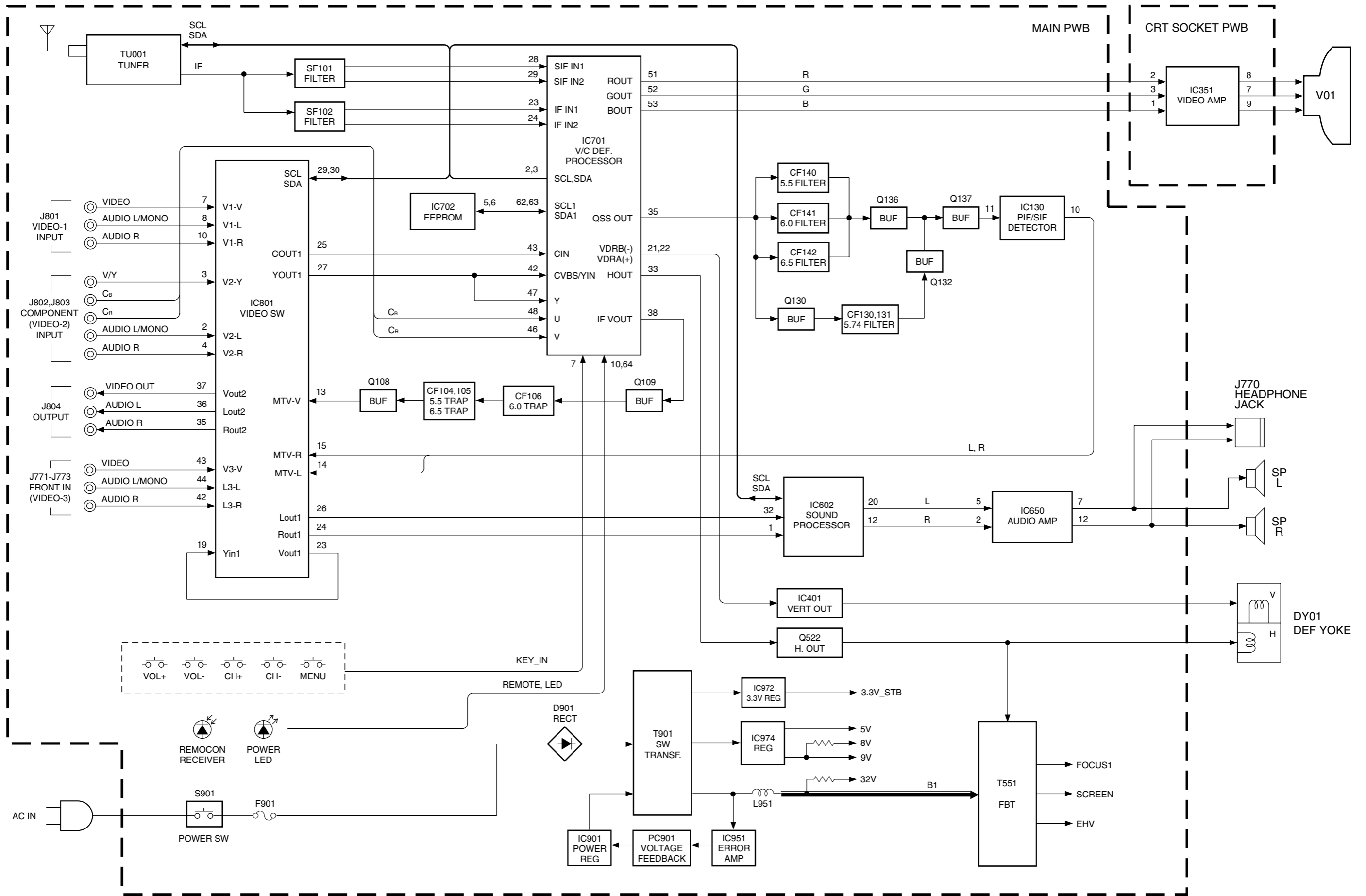
IC

BOTTOM VIEW	FRONT VIEW			TOP VIEW
				

CHIP IC

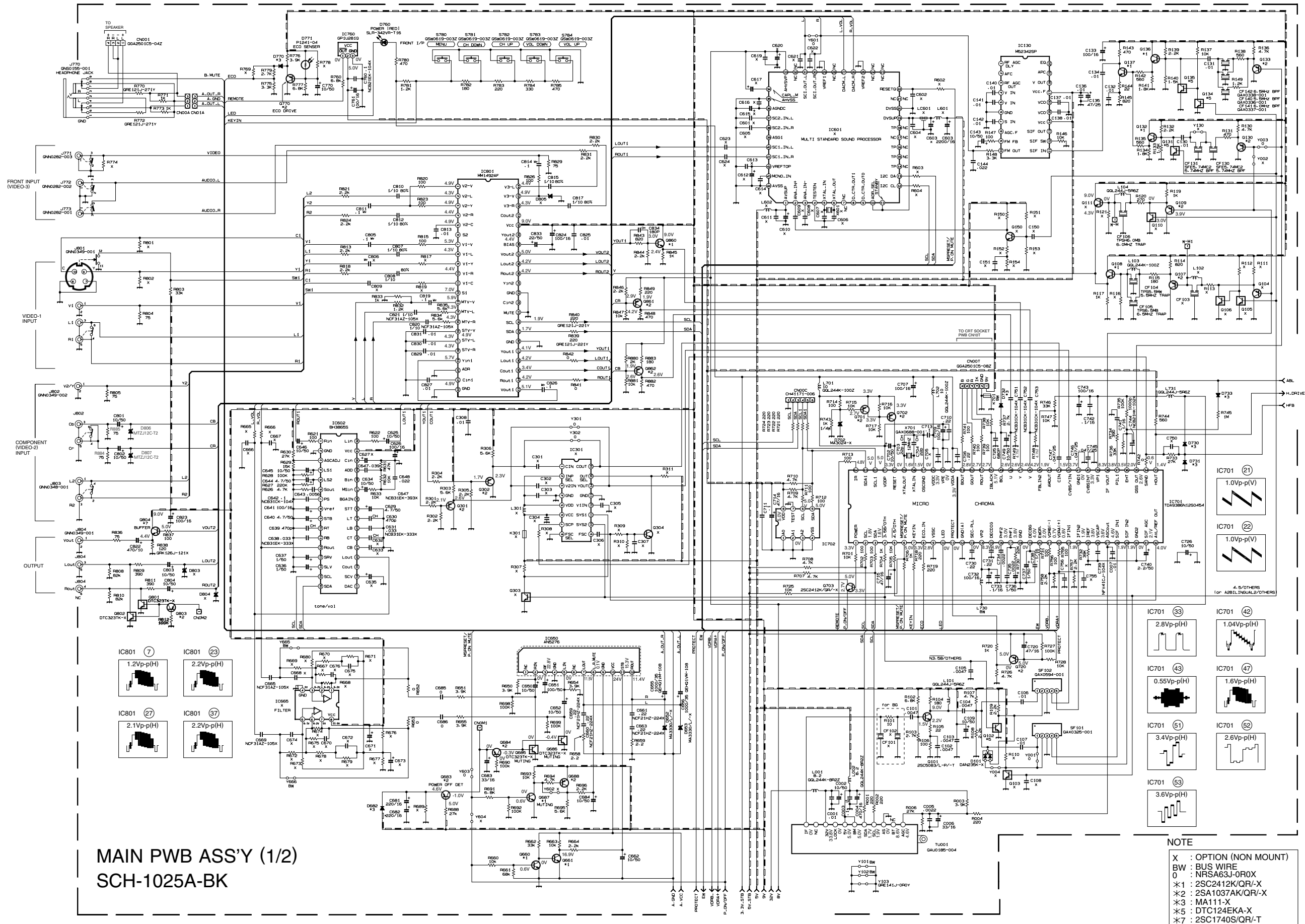
TOP VIEW	
	

BLOCK DIAGRAM

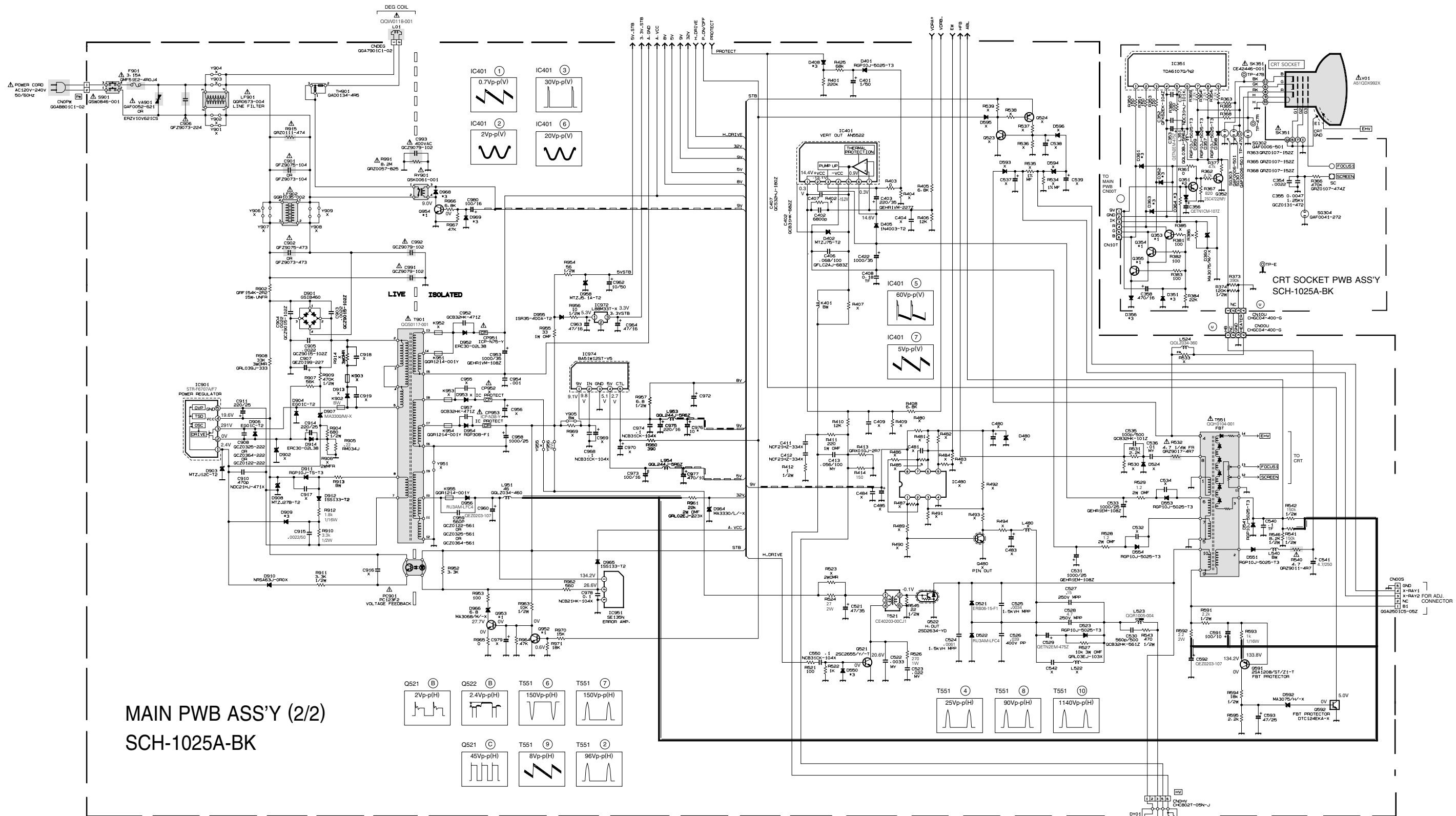


CIRCUIT DIAGRAMS

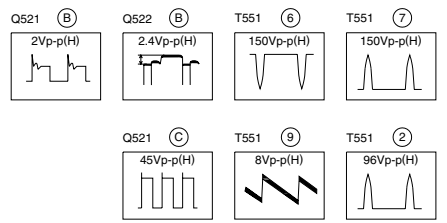
MAIN PWB CIRCUIT DIAGRAM (1/2)



MAIN (2/2) AND CRT SOCKET PWB CIRCUIT DIAGRAMS



MAIN PWB ASS'Y (2/2)
SCH-1025A-BK

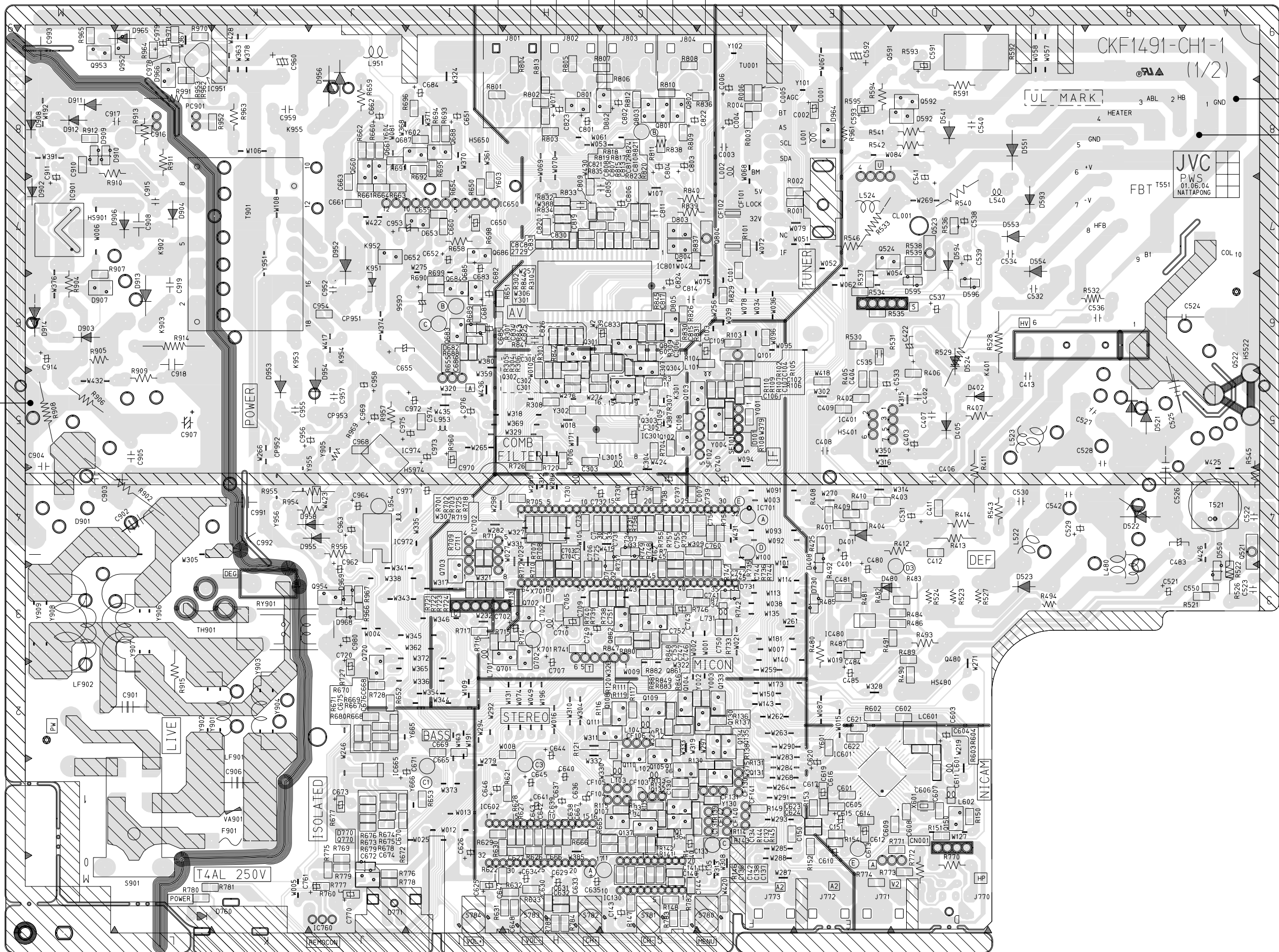


NOTE

- X : OPTION (NON MOUNT)
- BW : BUS WIRE
- 0 : NRSA63J-0R0X
- *1 : 2SC2412K/QR-X
- *2 : 2SA1037AK/QR-X
- *3 : MA111-X

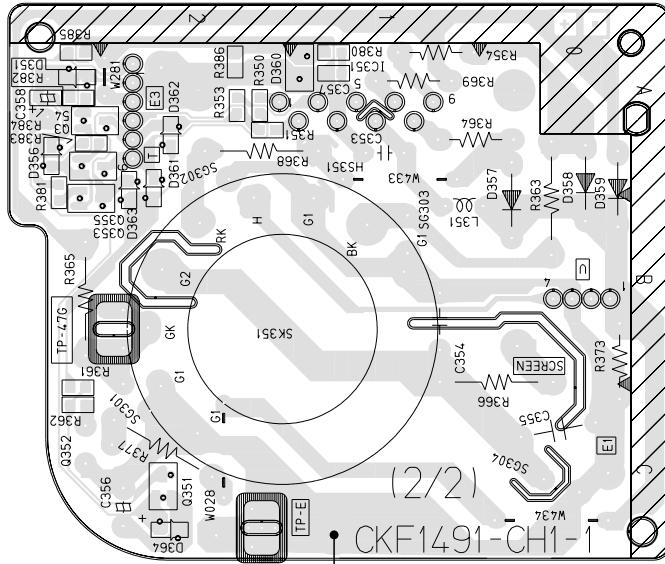
PATTERN DIAGRAMS
MAIN PWB PATTERN

(T) ↓
FRONT



B1
(H)

CRT SOCKET PWB PATTERN



TOP
↑

(H)



JVC

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